

ABSTRACT OF THE DISCLOSURE

A positive side conductor and a negative side conductor of an input terminal electrically connected to semiconductor elements, are electrically insulated from each other, and are laminated with each other, and the input terminal having such a laminated structure, an output terminal and substrates mounted thereon the semiconductor elements are arranged in a checkered pattern in a container. Further, the semiconductor elements mounted on the substrates, the input terminal and the output terminal are electrically connected to one another so as to obtain a loop-like electric path on a conductive member, thereby it is possible to aim at miniaturizing the power conversion apparatus and lowering the inductance thereof.

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